#### A Framework for Mission Assurance Exploiting Automation

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#### Maritime domain awareness

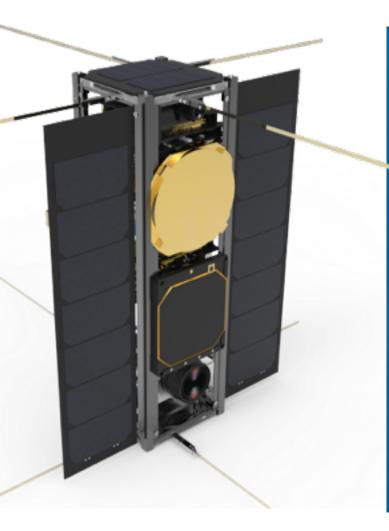


The nano-satellite constellation enables the effective monitoring of South Africa's extensive ocean and coastal areas to facilitate national maritime domain awareness.

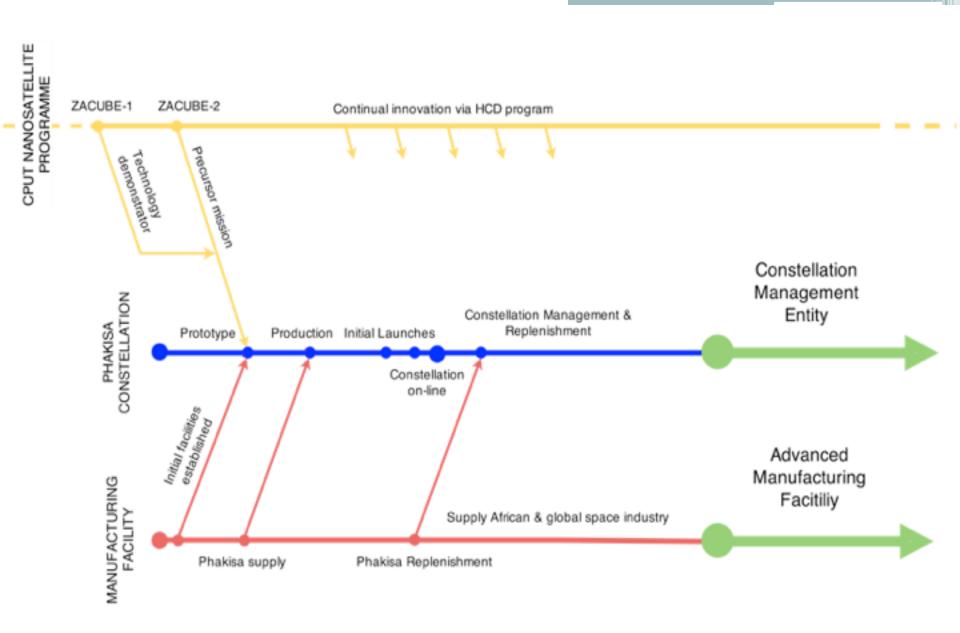
#### **Benefits:**

- effective vessel tracking
- enhanced security and safety
- monitor activities within ocean economy
- marine protection
- maritime trade information

#### ZACUBE-2 | MDA Precursor



- Feature AIS/VDES vessel tracking capability
  - Flexible Software Defined Radio to enable rural connectivity to remote health clinics and educational facilities
- Medium resolution imager to monitor field fires, oil spills, ...
- Launch-ready 2017



## Verification & Validation (V&V)

- 1992 survey of 2500 spacecraft failures 1962-1988
  - (Musgrave, Larsen & Sgobba 2009)

## • 48% of nanosats survive after launch

(Bouwmeester & Guo 2010)

Failure Cause	%
Design	24.8
Environment	21.4
Operations	4.7
Parts	16.3
Quality	7.7
Other	6.3
Unknown	18.9

Module or Subsystem	%
Bus	73.3
Telemetry, Tracking and Command	24.6
Guidance and Navigation	13.6
Electric Power	13.2
Data Handling	9.1
Thermal Control	5.6
Propulsion	3.7
Structure	3.5
Payload	<b>26.</b> 7
VIS and IR Optical	13.1
Comms	5.2
Special Payloads	4.9
Navigational	3.5

## Testbed (Functional & Environmental)

- Student project
- LabWindows<sup>™</sup>/CVI



LabWindows/CVI Real-Time

Reliable Test, Deterministic Performance in C

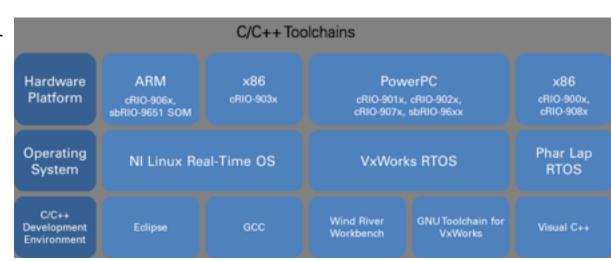
- C for Virtual Instrumentation with Real Time Module
- IDE, GUI development, ATE drivers, measurements, analysis
- Communication interfaces
- Automatic Test Equipment
- Thermal chamber





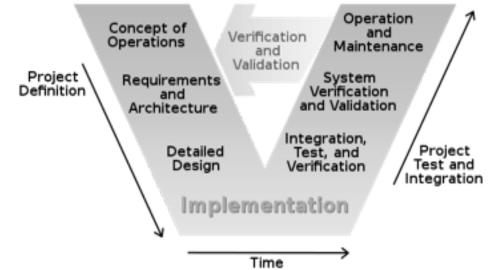
#### Futuristic Development/Test/V&V in C

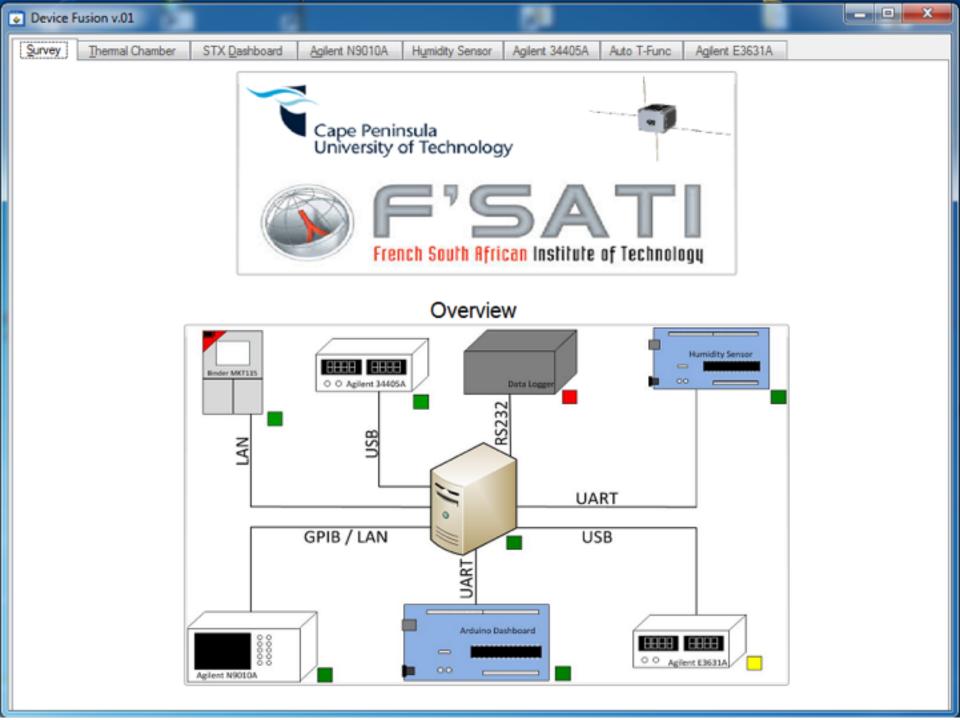
- Control ATE using algorithms e.g., in-orbit wattage profile, power management
- C data types: access to ext. sims thru TCP/IP sockets
- Interface with HDL EDA (VHPI/VPI)
- Custom protocols e.g., for thermal chamber
- FPGA interface C API
- Low-level system debugging and precision execution



#### **Goals of Automation**

- Co-engineering
- Efficiency (time, resource and cost control)
- Acceptance level testing
- Product profiling, test report
- Theses and internships
- Not to find hardware/ software bugs





#### Testbed & Automation Features

- Event-driven, human interface
- DUT configuration (e.g., FPGA registers)
- Encapsulation
  - Test methods and test logic
  - ATE configuration
- Measurement visual display
- Ilities
  - Modular (multiple, independent developers)
  - Readable (common enforced style for VIs)
  - Tabular (abstraction for test centric, ATE centric and product centric views)
  - Scalable (limited versions)
  - Re-usable (common ATEs, tests methods)
  - Adaptable (product variants)

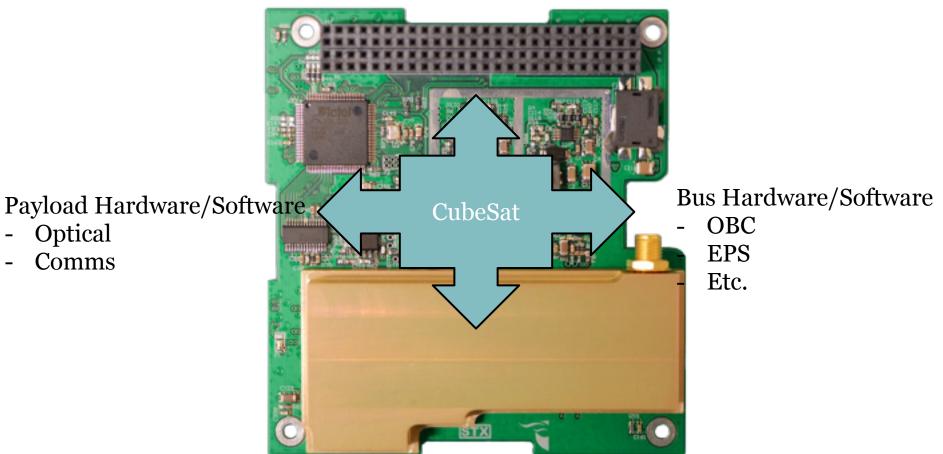
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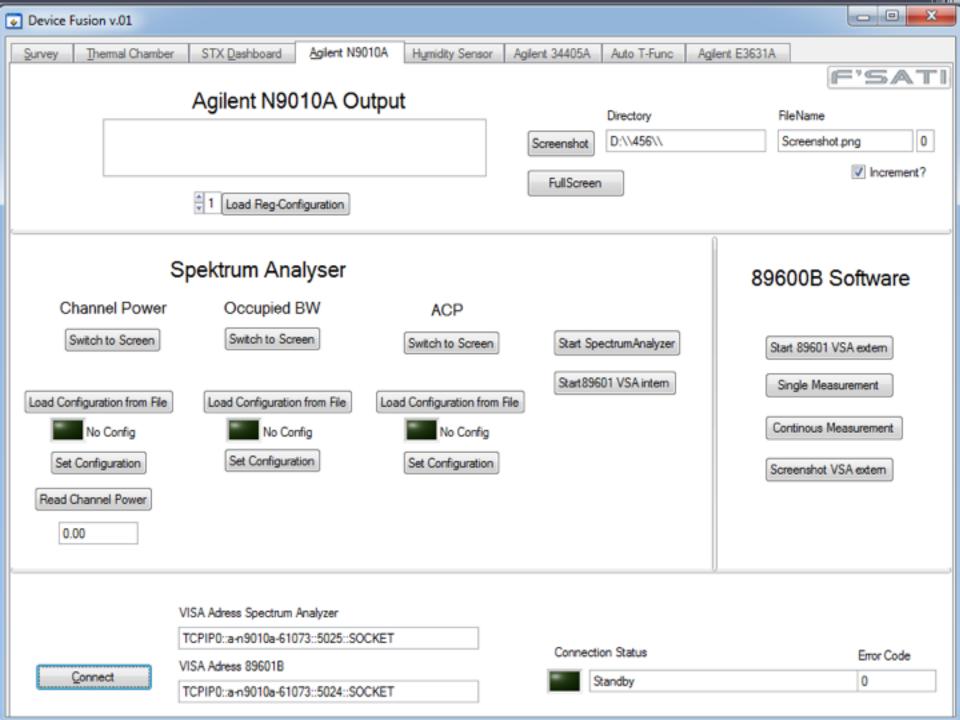
HIMA ATT

#### Functional Test Loop

Simulators, Emulators, Debuggers



ATE on Instrumentation Bus



#### Thermal Test Loop

Debuggers

Payload Hardware/ Software

- Optical
- Comms

Bus Hardware/ Software

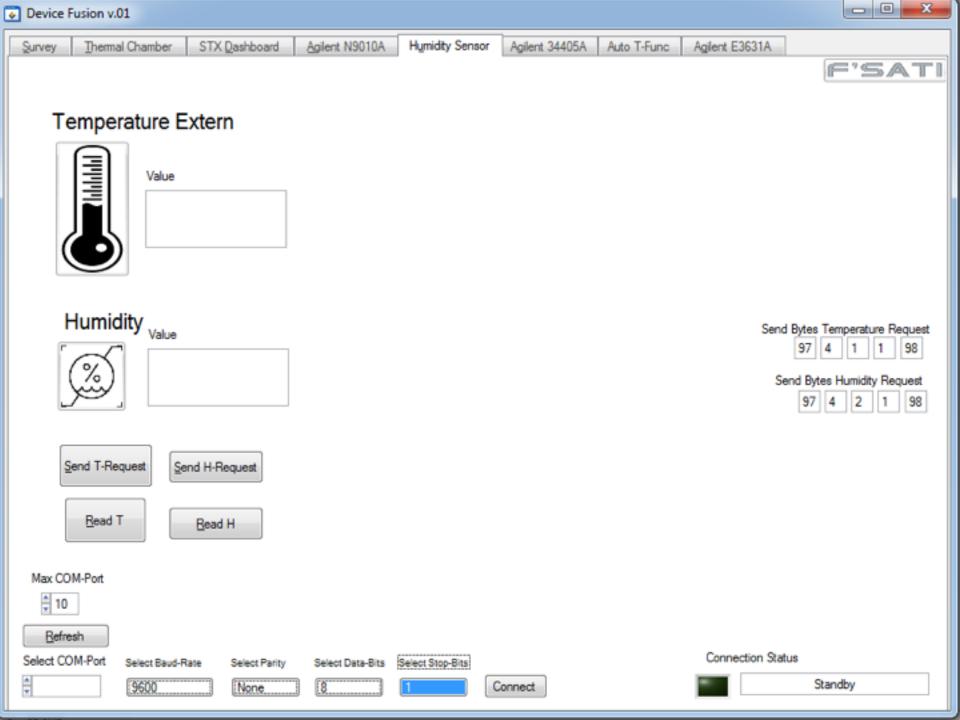
- OBC
- EPS
- ADCS

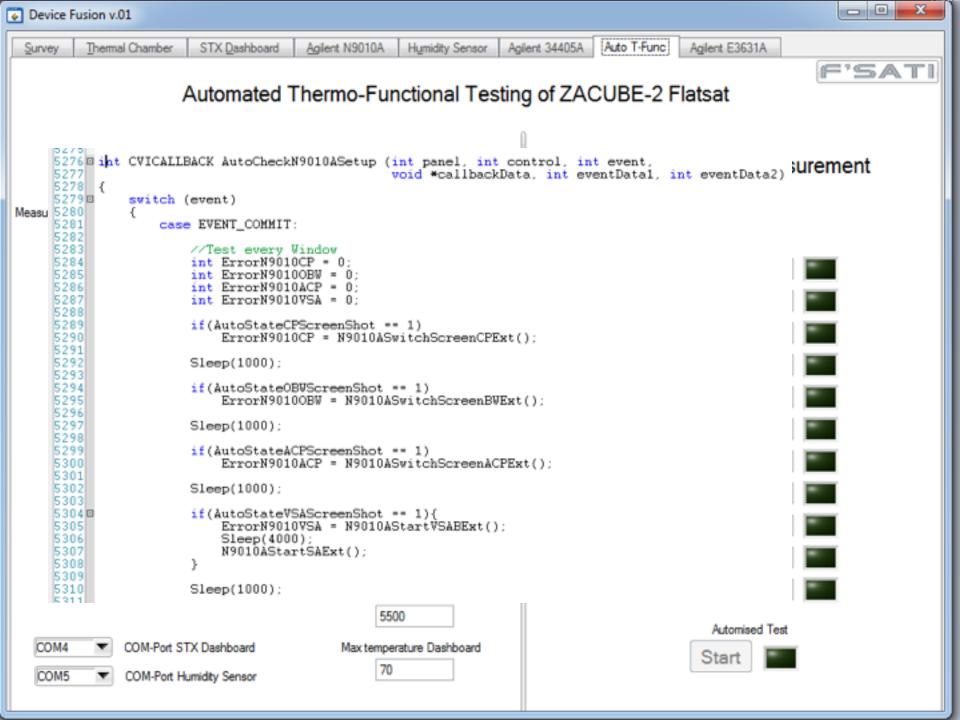
ATE on Instrumentation Bus

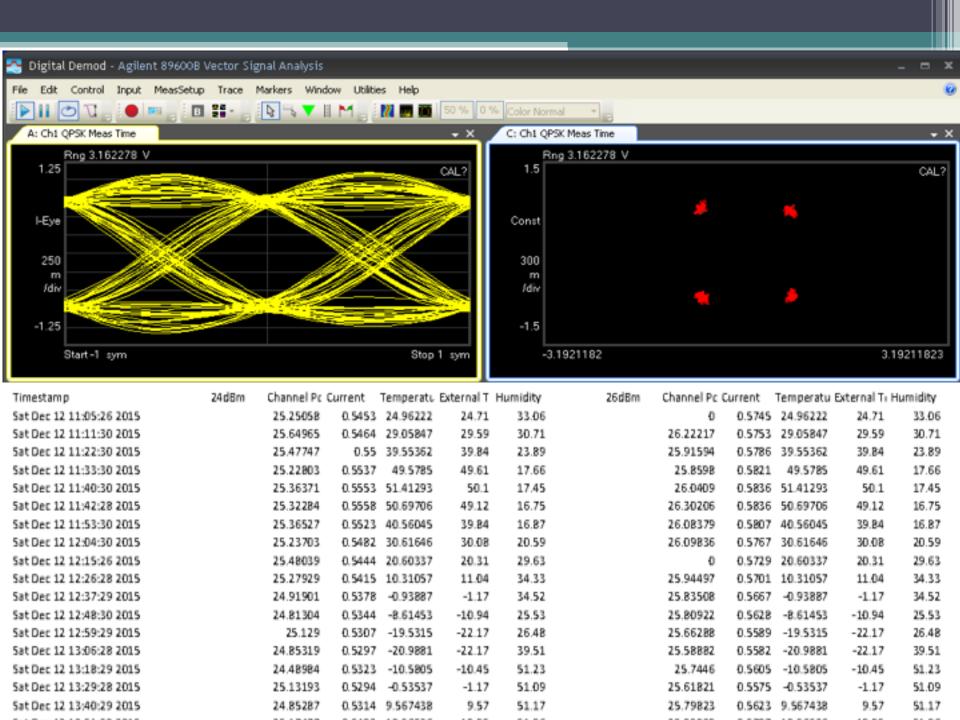
Device Fusion v.01

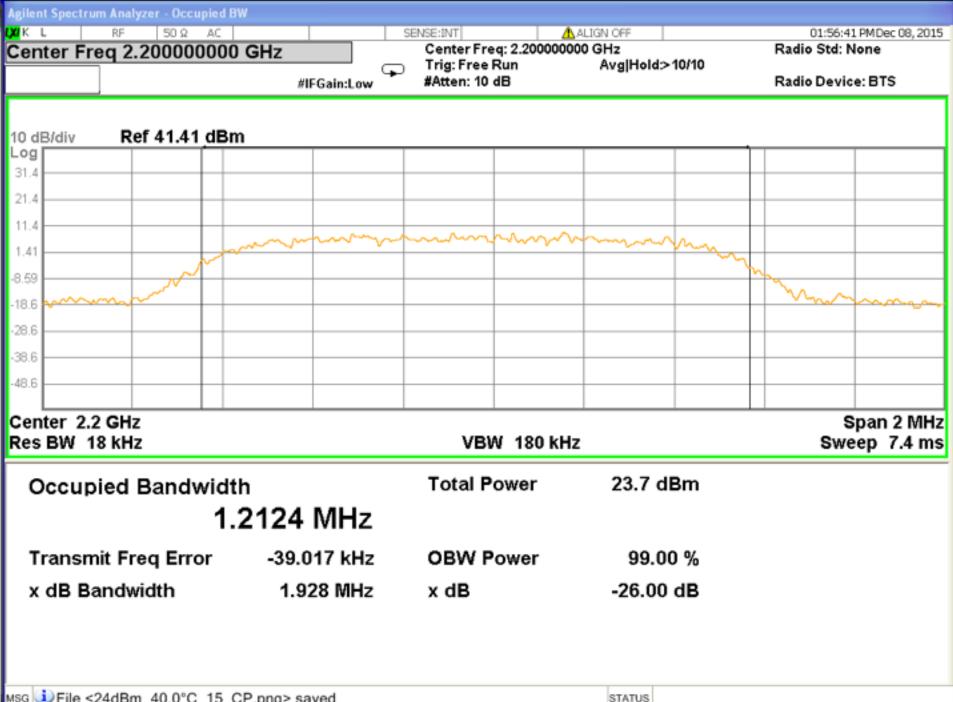
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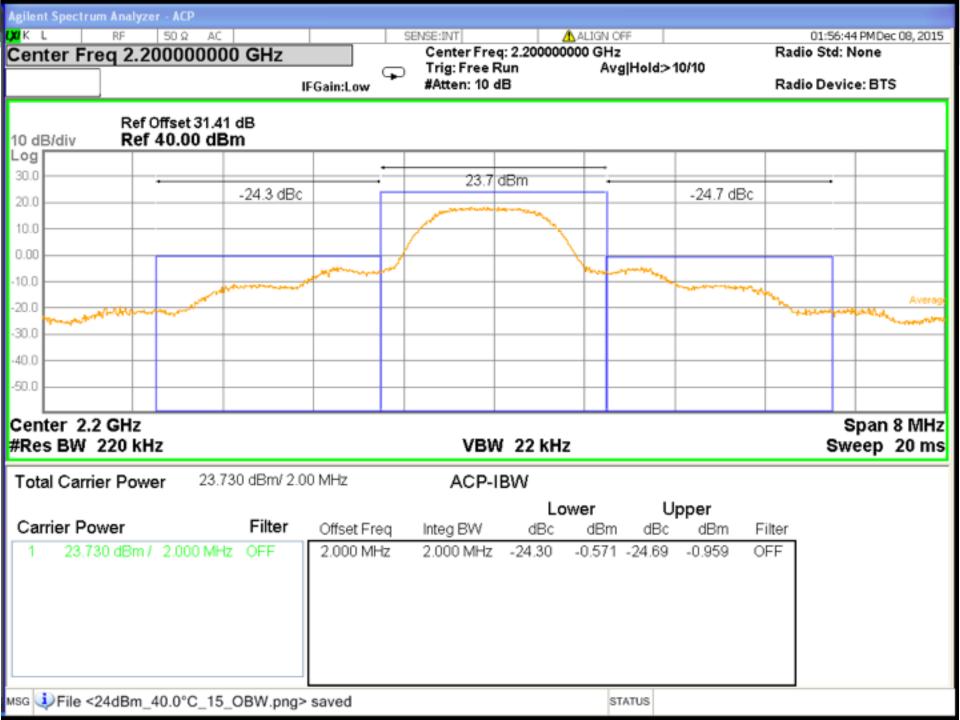








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#### WiP

# Compact TM & Command Radio Future Work

- Helmholtz coil
- EPS simulation/in-orbit solar panel emulation
- Susceptibility test in reverberation chamber
- OBC
- HIL
- EM model





