#### **The Colorado Student Space Weather Experiment (CSSWE)**

#### **Command & Data Handling**

Muralikrishna Nallamothu David Gerhardt Drew Turner Advisors: Professors Scott Palo & Xinlin Li

2010 Summer CubeSat Developer's Workshop Logan,Utah August 8, 2010

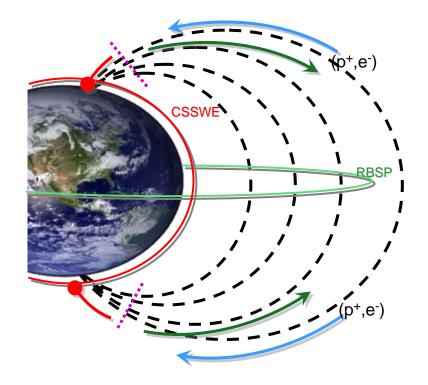




### **Objectives**

#### Science Objectives

- Understand the relationships between solar energetic protons (SEPs), flares, and coronal mass ejections (CMEs)
- Characterize the variations in Earth's radiation belt electrons
- High energy solar particles (p<sup>+</sup>, e<sup>-</sup>) travel along magnetic field lines
  - Oscillate between N and S magnetic pole





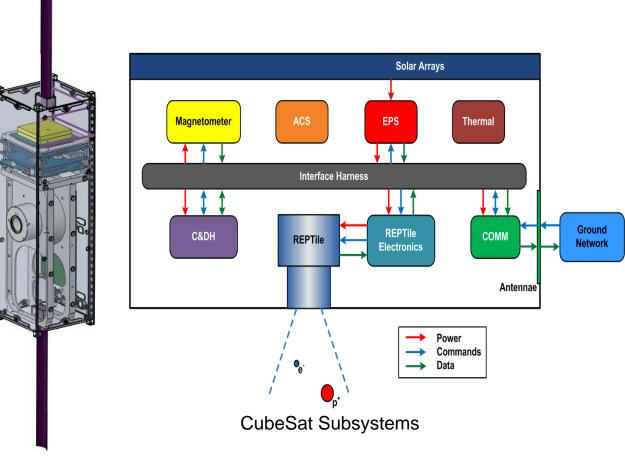




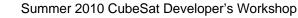
### **CubeSat Overview**

#### Subsystems

- EPS
- C&DH
- REPTile
- Structures
- ADCS
- Systems
- COMM
- INTF
- Thermal





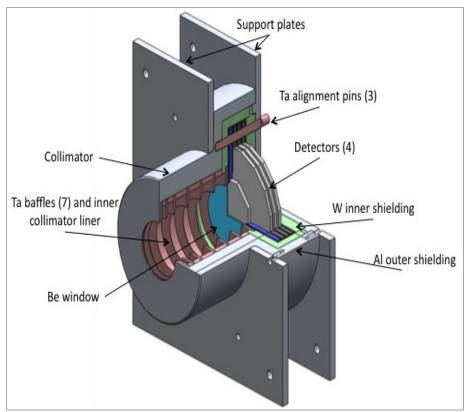






### REPTile

- Relativistic Electron and Proton Telescope integrated little experiment (REPTile) to measure dangerous energetic electrons and protons
- Motivation
  - Potentially fatal to spacecraft and astronauts
  - April 5, 2010 Intelsat Galaxy 15 fails due to unexpected particle flux increase

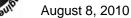


#### REPTile





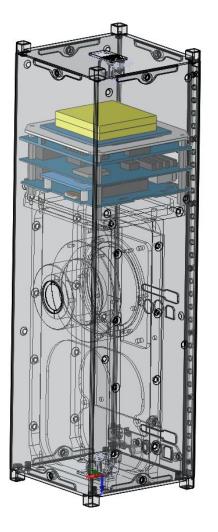




### **C&DH Overview**

#### Requirements

- Command REPTile to take science data
- Gather HK data from the CubeSat subsystems
- Perform fault detection and correction (FDC)
- Send science and housekeeping data to COMM
- Receive commands from ground station
- Provide a single-wire reset to all powered subsystems



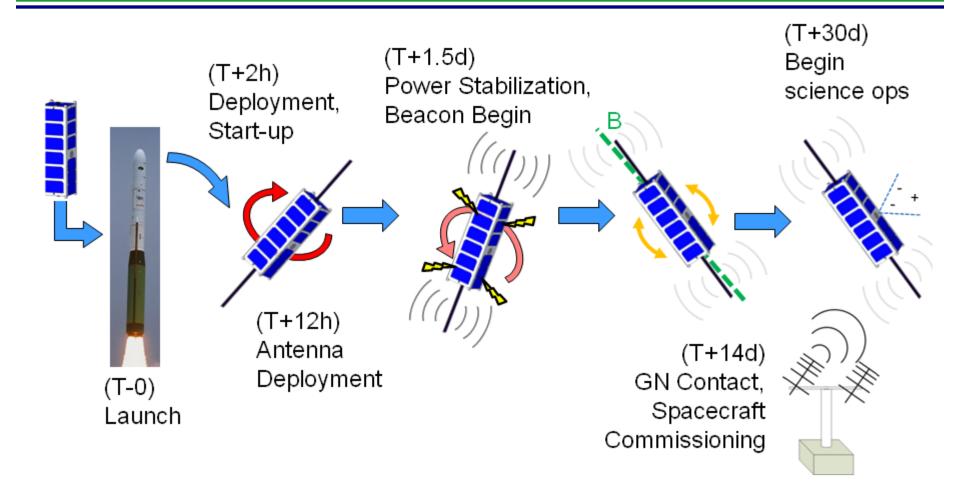


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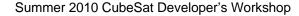




### **Mission Timeline**



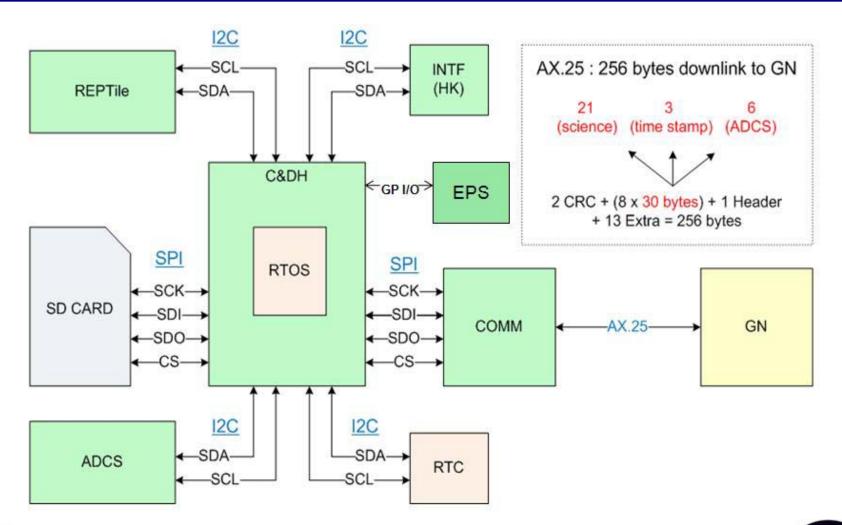








# **C&DH Block Diagram**





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#### **C&DH Hardware - Pumpkin**

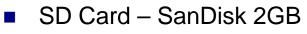
- The flight module has two components
  - Motherboard
  - PPM A3 MSP430F2618
- Both are provided by Pumpkin



PPM



**Motherboard** 



- Pumpkin development board Pumpkin Inc.
- TI MSP430F2618 64 pin target board + debug tools



Pumpkin Dev. Board



TI MSP430 64 pin target board

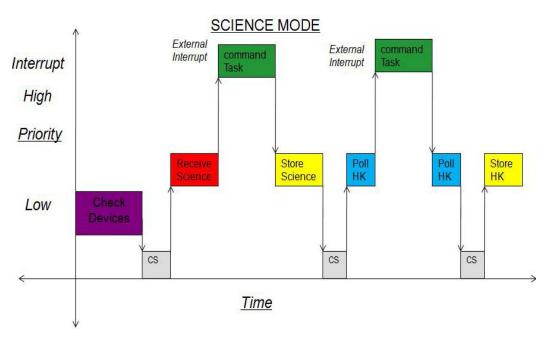






# **C&DH Software**

- Salvo Real-time Operating System (RTOS)
  - Co-operative RTOS
  - Works with CrossWorks IDE (Rowley Associates)
- EFFS-THIN Library HCC Embedded
  - API for accessing file-system on SD-Card
  - Library only available

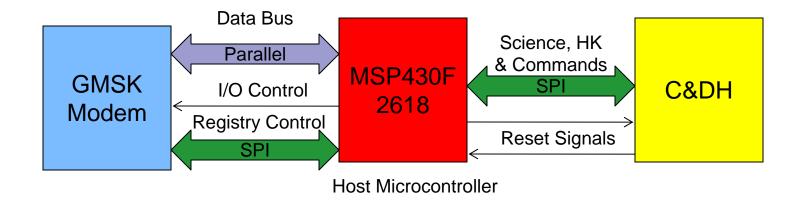








### **C&DH- COMM Interface**









### **REPTile Data – C&DH**

- C&DH collects REPTile data
  - Every 6 seconds (top most priority receiveTask)
  - Useful Science Data at high latitudes
- Data dumped based on Two Timestamps
  - Timestamps resistant to C&DH reset

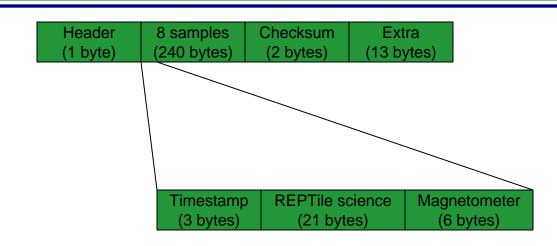
- REPTile
- Magnetometer
- Timestamp
- Housekeeping
- Packet Margin 13.2%
- 545,642 bytes of data every day







### **Science Data Packet Format**



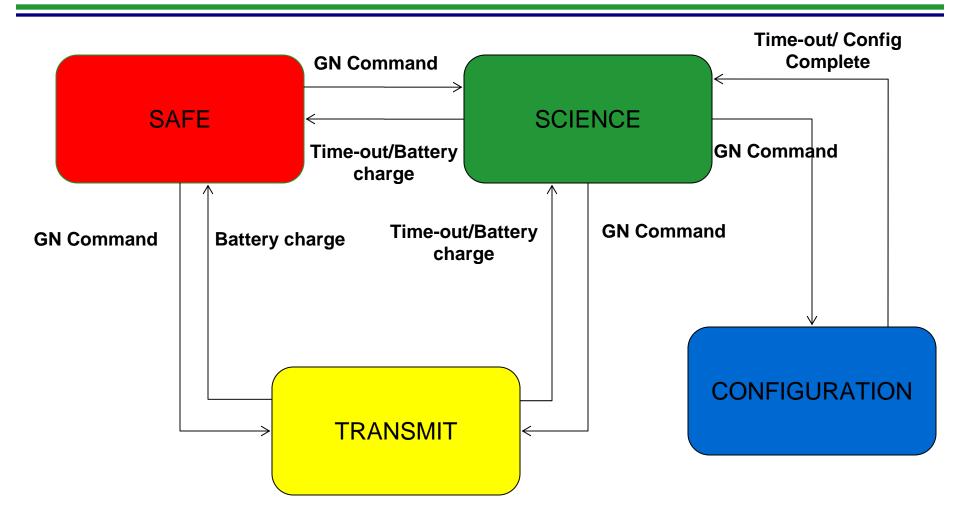
- Science data stored on SD card in samples (30 bytes)
- Science data packetized in 256 byte chunks (AX.25)
  - 1 packet per 48 seconds = 461 KB/day
  - Note, not all of the science data will be down-linked
- 3 byte timestamps allows for 6 months with 1 second resolution







#### **C&DH Architecture – Operational Modes**

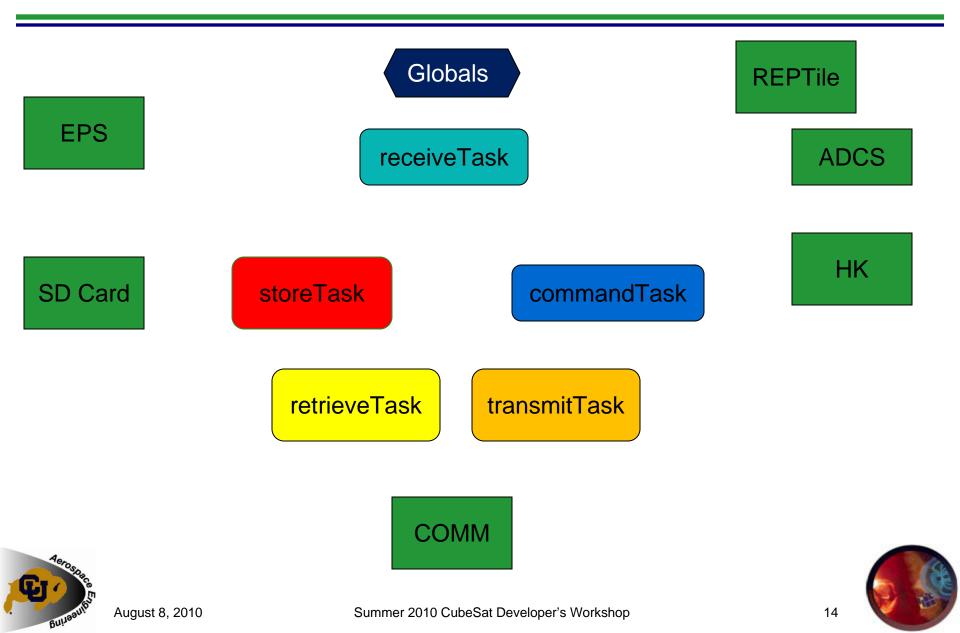






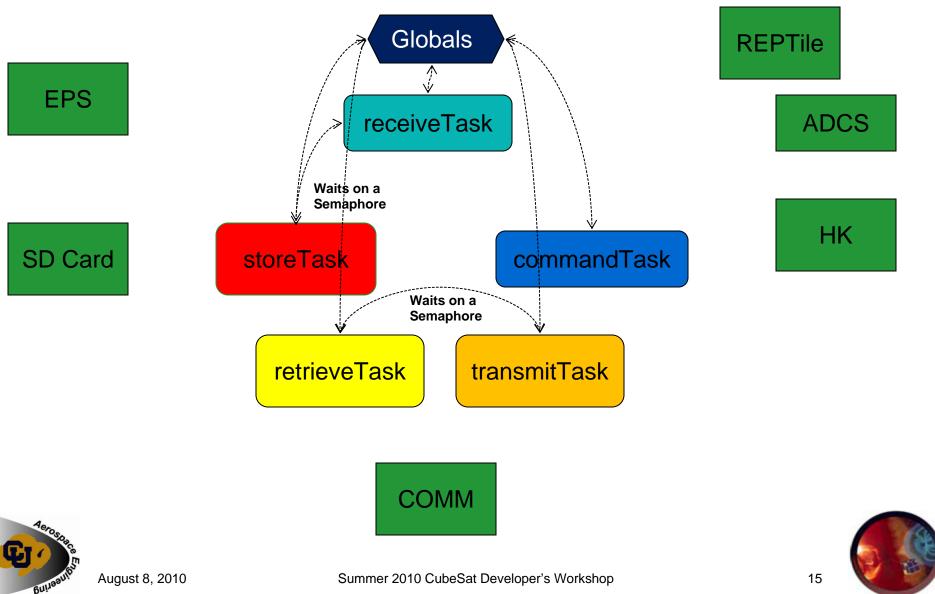
#### **Tasks**





#### LASP

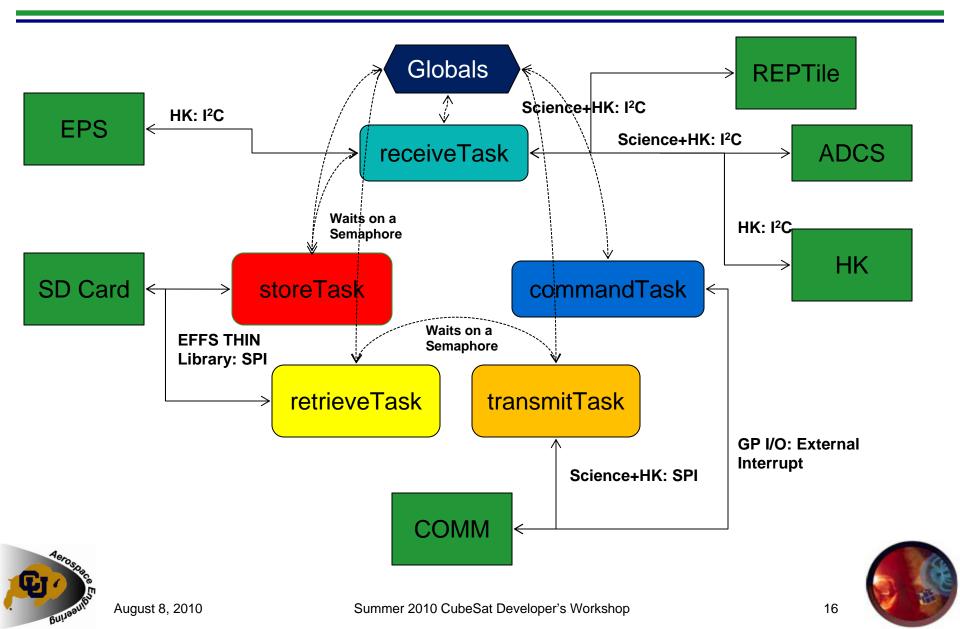
#### **Tasks**



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#### **Tasks**







- C&DH Operational Modes
- RTOS vs Infinite Loop
  - Decision should be based on a thorough study of requirements
- Communication Interfaces SPI, I<sup>2</sup>C
  - SD Card
  - SPI faster throughput than I<sup>2</sup>C







### Thank You !!

### Questions ?





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# **BACKUP SLIDES**





Spring 2010 End Semester Review



#### **C&DH Architecture – Operational Modes**

- SAFE
  - Beacon 0 Lowest Power Mode (C&DH+COMM)
    - Battery 25% charge, Lowest Beacon Rate
  - Beacon Min C&DH + COMM + EPS HK
    - Battery 40% charge, Min. Beacon Rate
  - Beacon Max C&DH + COMM + All HK, Max. Beacon Rate
    - Battery 80% charge, Max. Beacon Rate

- SCIENCE
  - scienceTake Turn on REPTile, I<sup>2</sup>C Ready
    - Poll EPS HK, Turn on Reptile, USCI\_B0 Ready
  - scienceReceive dedicated I<sup>2</sup>C (USCI\_B0 Module), shared I<sup>2</sup>C (USCI\_B1 Module)
    - Receive REPTile, ADCS science data
  - RTCReceive shared I<sup>2</sup>C (USCI\_B1 Module)
    - Receive Time-stamp data from RTC
  - scienceStore dedicated SPI (USCI\_A1 Module)
    - Store Science Data Sample in SDCard
  - HKReceive shared I<sup>2</sup>C (USCI\_B1 Module)
    - Receive HK data
  - HKStore dedicated SPI (USCI\_A1 Module)
    - Store HK Data Sample in SDCard







#### **C&DH Architecture – Operational Modes**

#### Transmit

- scienceRetrieve dedicated SPI (USCI\_A1 Module)
  - Retrieve relevant time-stamp science data from SD Card
- HKRetrieve dedicated SPI (USCI\_A1 Module)
  - Retrieve relevant time-stamp HK data from SD Card
- sendData dedicated SPI (USCI\_A0 Module)
  - Send Science, HK data samples to COMM via USCI\_A0

#### Configuration

- Default
  - Configuration parameters, change mode







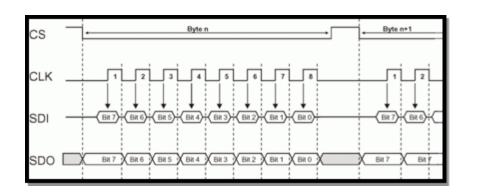
### **Communication Interfaces**

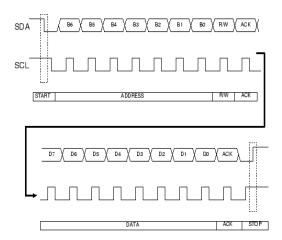
#### SPI

- Full duplex communication
- Higher throughput than I<sup>2</sup>C

#### ■ |<sup>2</sup>C

- Half duplex communication
- 100 kbit/s (upto 3.4 Mbit/s)



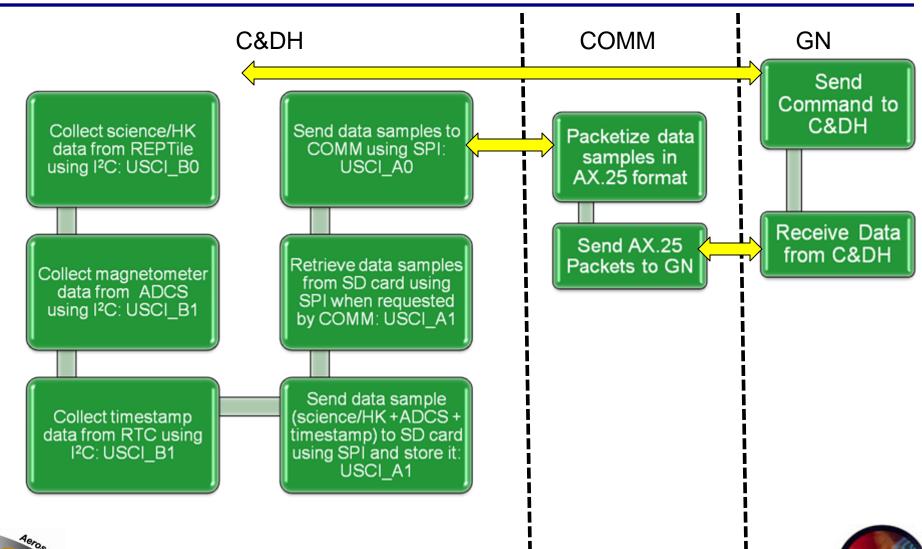








#### **C&DH-GN Task Flow - Science**

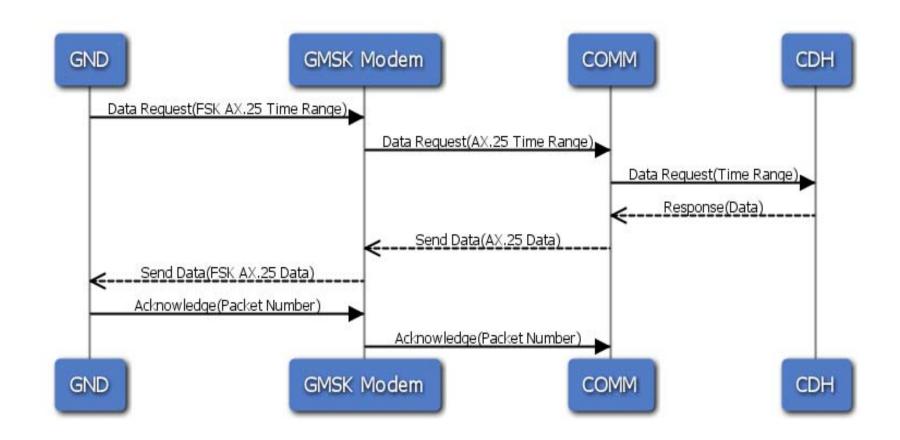




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### **C&DH – GN Task Flow**









#### **Command Data Packet Format**

Header (1 byte) Sectional Header (optional) (2 bytes)	Function Op-code (2 bytes)	Parameters (n bytes)	Extra (256-(n+5) bytes)
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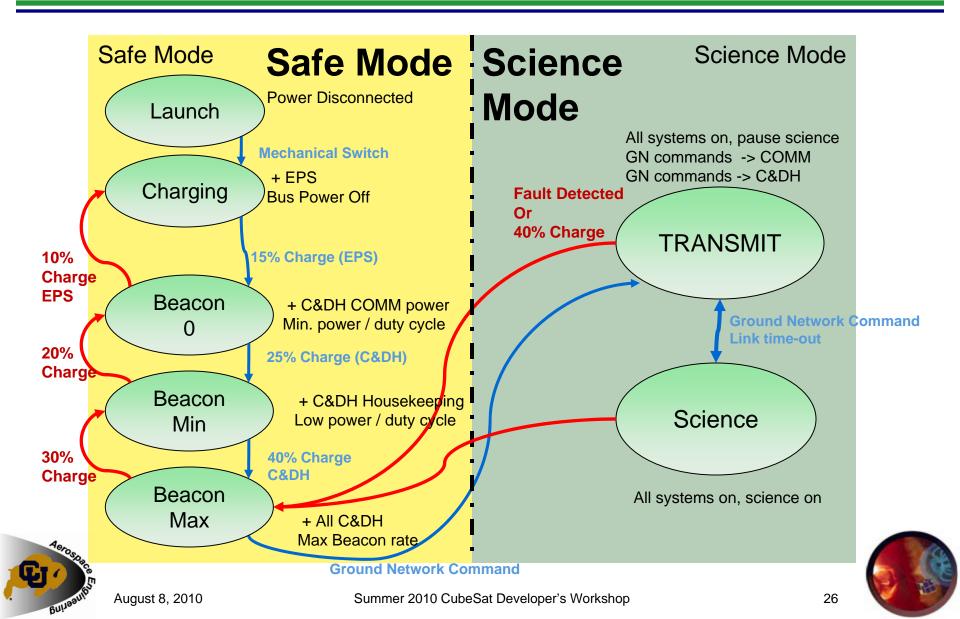
- Command Data Packet routed by COMM to C&DH
- Data packet size follows AX.25 format– 256 bytes
- Functional op-code identifies configuration parameters Beacon period, HK Data collection frequency, depth of discharge for safe mode, ignore HK data, time-out, turn off specific detectors, C&DH reset (by COMM)





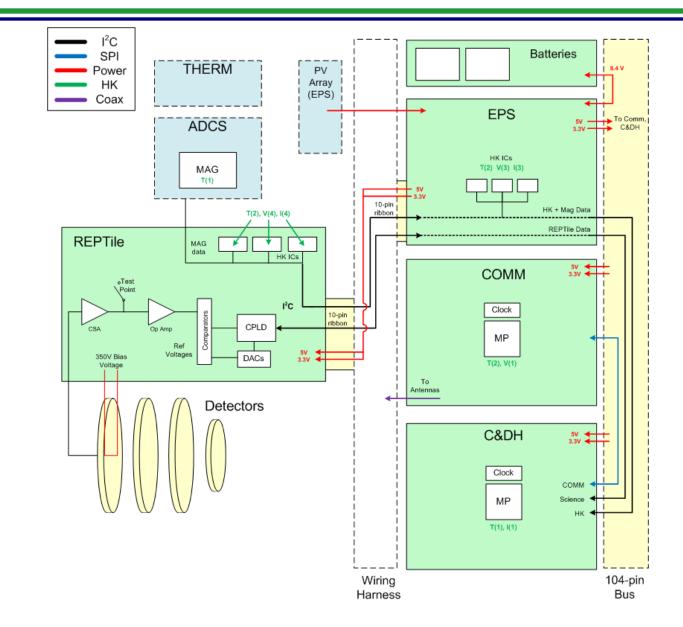


### **Operational Modes**





### **INTF Block Diagram**





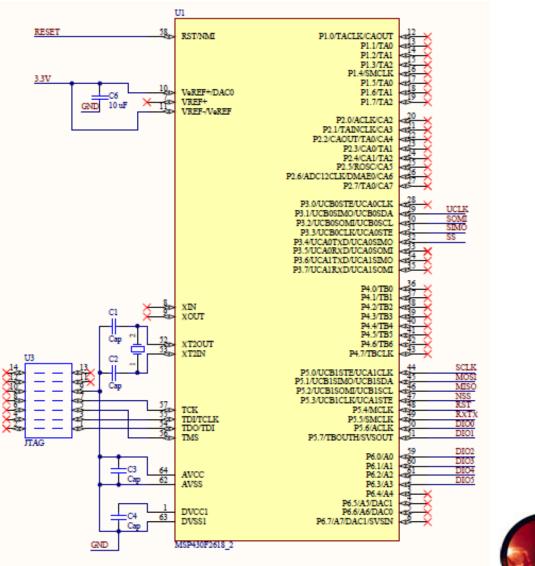




#### **Microcontroller** Unit

#### MSP430F2618 MCU

- Low power
- Shared design and programming environment with C&DH
- Shared learning curve with C&DH
- Relatively few external components required

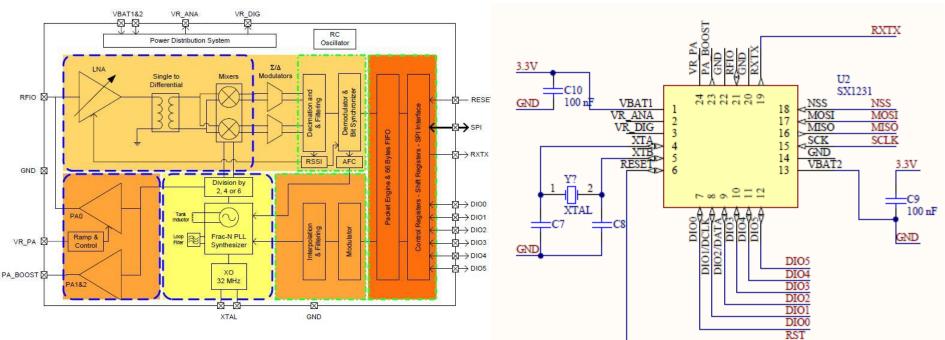








### **GMSK Modem**



SX1231 UHF Modem Transceiver

- GMSK Modulation
- Provides majority of Front End
- Requires few external components
- Low power consumption
- Variable transmit power



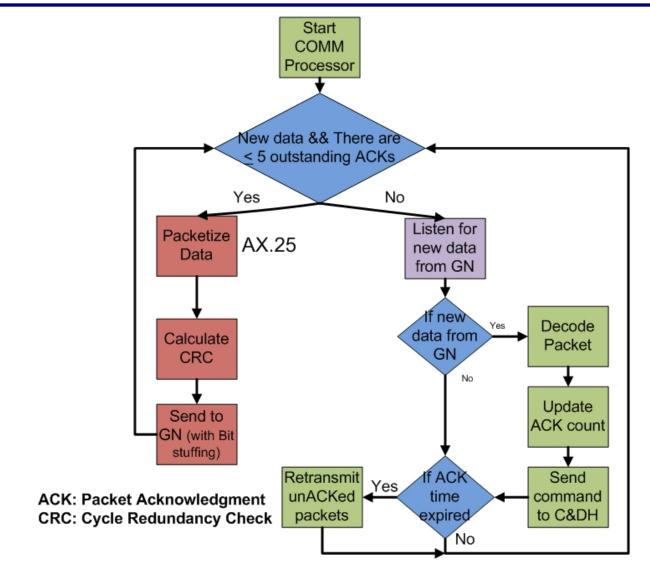
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### **Software Flow Diagram**





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#### **C&DH - Tasks Interaction**

